

Notice of References Cited

Application/Control No.

09/821,485

Applicant(s)/Patent Under
Reexamination
SULLIVAN ET AL.

Examiner

Kim T. Huynh

Art Unit

2112

Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
<input checked="" type="checkbox"/>	A	US-5,890,207 A	03-1999	Sne et al.	711/113
<input checked="" type="checkbox"/>	B	US-6,289,401 B1	09-2001	Tuccio et al.	710/8
<input checked="" type="checkbox"/>	C	US-2002/0116564	08-2002	Paul et al.	710/301
<input checked="" type="checkbox"/>	D	US-2003/0069889	04-2003	Ofek, Yuval	707/100
<input checked="" type="checkbox"/>	E	US-2003/0088626	05-2003	GUPTA et al.	709/206
<input checked="" type="checkbox"/>	F	US-2002/0007428	01-2002	CHILTON, KENDELL ALAN	710/52
<input checked="" type="checkbox"/>	G	US-5,890,219	03-1999	Scaringella et al.	711/162
<input checked="" type="checkbox"/>	H	US-6,052,302	04-2000	Moyer et al.	365/173
<input checked="" type="checkbox"/>	I	US-6,593,795	07-2003	Cho, Shizuo	327/333
<input checked="" type="checkbox"/>	J	US-5,751,740	05-1998	Helbig, Sr., Walter A.	714/768
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
<input checked="" type="checkbox"/>	U	SRAM Based re-programmable FPGA for Space applications, Wang, IEEE Transactions on Nuclear science, Vol. 46, No.6, Dec 1999
<input checked="" type="checkbox"/>	V	Advanced Analog Circuit Design on a Digital Sea-of-Gates Array, Van Dongen and Rikkink, 1994, IEEE
<input checked="" type="checkbox"/>	W	Channel Routing with Non-Terminal Doglegs, Byan Preas, 1990, IEEE
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.